

ABSTRACT OF THE DISCLOSURE

An image sensor clock driver for reducing energy consumption comprises a charge-coupled device having at least two gates for receiving an electrical signal; at least first and second electrical buses each having a unique
5 voltage; a first switch on each of the gates that connects each gate to any one of the electrical buses or to a neutral position; and a second switch to connect the two gates together for reducing power consumption by transferring charge from one gate to the other gate at a time when the first switches are in a neutral position.

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